

In Place of FORM PTO-1449 (Modified)

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Applicants: Jente B. Kuang et al.

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**LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANTS' INFORMATION DISCLOSURE  
STATEMENT**

Reference Designation

**U.S. PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
___ AAA						
___ ABA						
___ ACA						
___ ADA						
___ AEA						
___ AFA						
___ AGA						
___ AHA						
___ AIA						
___ AJA						
___ AKA						
___ ALA						
___ AMA						

**FOREIGN PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
___ ANA						
___ AOA						

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

Examiner  
Initial

APA Rafik S. Guindi et al. "Design Techniques for Gate-Leakage Reduction in CMOS in Circuits," IEEE, 2003, 5 pages.

\_\_\_ AQA

\_\_\_ ARA

\_\_\_ ASA

Examiner:

Z. An

Date Considered:

6/7/05

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.